

Patent Application No. 09/338,035
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IN THE DRAWINGS:

New Figures 6, 7 and 8 (single sheet) are presented herewith for the Examiner's review and approval.



Henry Esmond Butterworth

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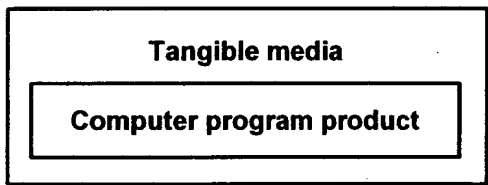


Fig. 6

If the task is not capable of fully loading into the instruction cache, logically dividing the task code such that least one atomic portion of the task code will fully load in the instruction cache

Fig. 7

Executing the task code for processing the new task in the instruction cache without loading new code into the instruction cache

Fig. 8